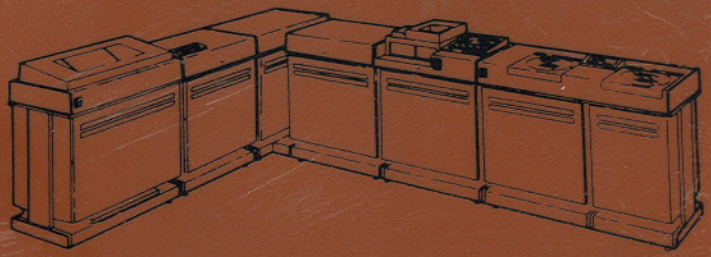
NCR

century 100

INTEGRATED SYSTEM



TECHNICAL INFORMATION HANDBOOK

CENTURY 100

INTEGRATED SYSTEM

TECHNICAL INFORMATION

HANDBOOK

(REVISED)

PROPERTY OF

THE NATIONAL CASH REGISTER COMPANY

DAYTON, OHIO 45409

May 31, 1969

MS 5025

INTRODUCTION

The primary purpose of this handbook is to provide the Technical Services Representative with handy, quick-reference information pertaining to the C-615-100 Integrated System. This information includes; tables, operation flow charts, service aids, and all Service Information Bulletins.

Space for notes is provided throughout the handbook. Extra filler paper for additional notes can be purchased at your local Stationary Store.

To make this handbook more useful to you, we want your comments and suggestions. Fill out the COMMENT SHEET in the back of this handbook and forward it to:

Technical Education Department
International Technical Services
The National Cash Register Co.
Main and K Streets
Dayton, Ohio 45409

Any additional information or service aids that you use and would like to see included in this handbook, should also be forwarded to the above address.

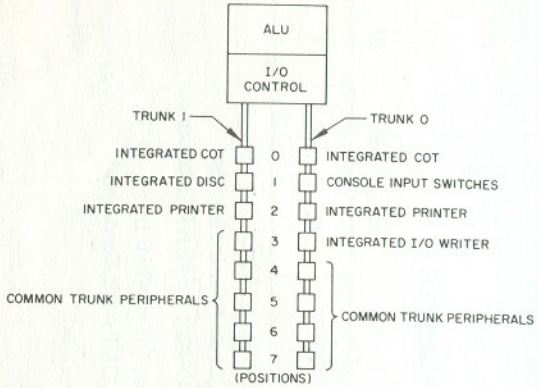
Any suggestion intended for award considerations, should be submitted using the normal suggestion procedures.

INTERNATIONAL TECHNICAL ASSISTANCE POLICY

The importance of promptly analyzing and resolving problems (of both technical service and programming) at Century system installations requires that this policy, now in effect, be followed:

Either the Technical Service Manager or the Field Supervisor (in charge of EDP) must be notified of the status of any system in trouble after two hours of down time, if the problem has not been resolved. This will permit timely planning for assistance, as needed.

PERIPHERAL ASSIGNMENT



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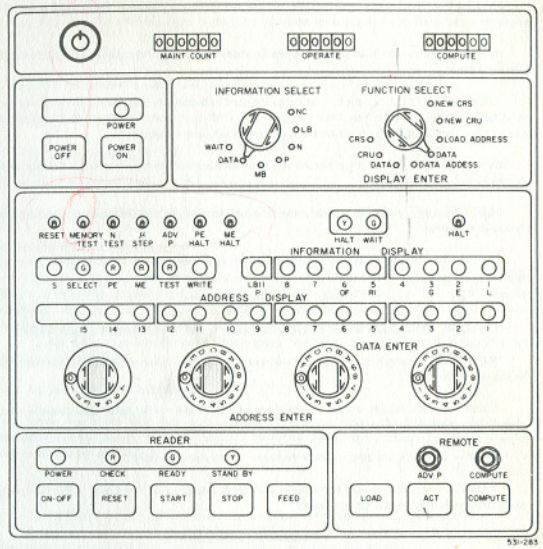
OPERATORS CONSOLE



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PROCESSOR CONTROL SWITCHES

POWER ON AND POWER OFF – Pressing the Power On switch causes the processor to Up-Sequence. When up-sequencing is completed, a.c. power is sent to the integrated peripherals. Pressing the Power Off switch causes the processor to down-sequence and remove a.c. power from the integrated peripherals.

INFORMATION SELECT – This switch is used to display the software wait messages as well as the contents of the various processor registers.

FUNCTION SELECT – This switch is used in conjunction with the Act Switch and the Address Enter switches to display or enter an address or data from the console.

RESET – This switch, when activated with the Halt switch ON, provides the processor and peripherals with a general reset and brings the processor to the Halt state.

MEMORY TEST – This switch is used in conjunction with the Maintenance key (test mode) to initiate the memory test operation.

N TEST – This switch is used in the test mode, to hold the processor in a particular N flow.

MICRO STEP AND ADVANCE P – These switches are used in the test mode, to single step the processor through an operation, one computer cycle at a time.